

FIG. 1

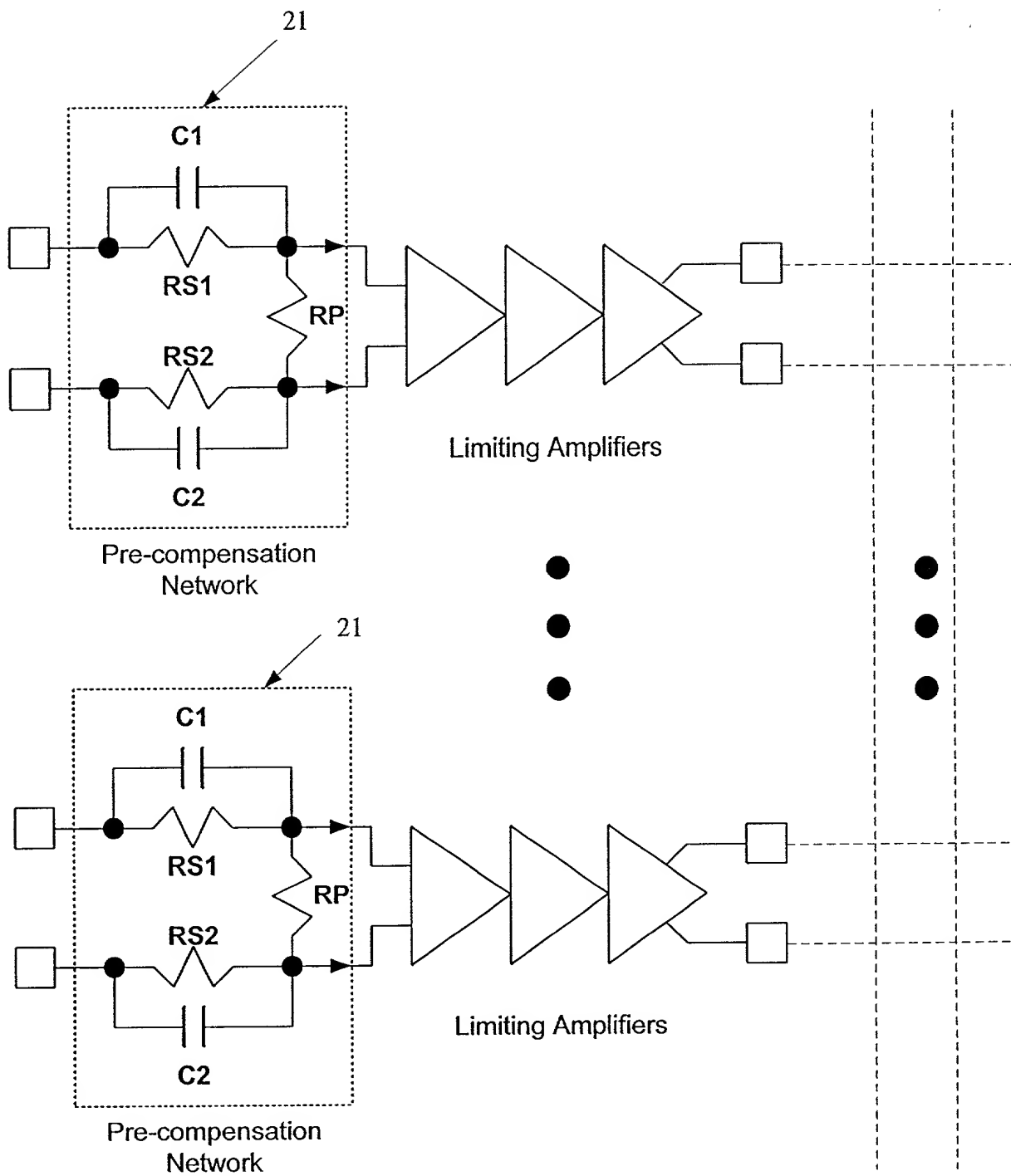


FIG. 2

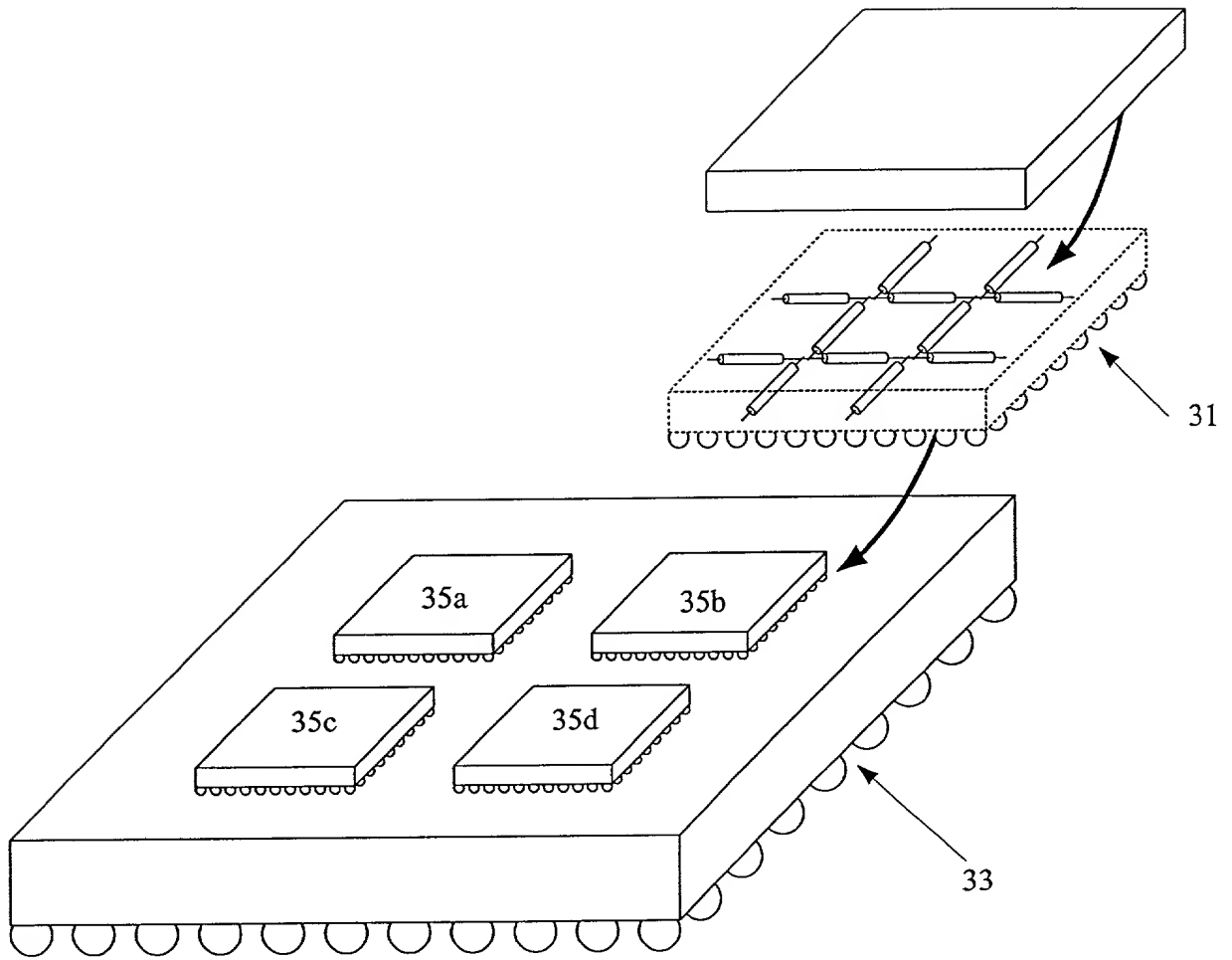


FIG. 3A

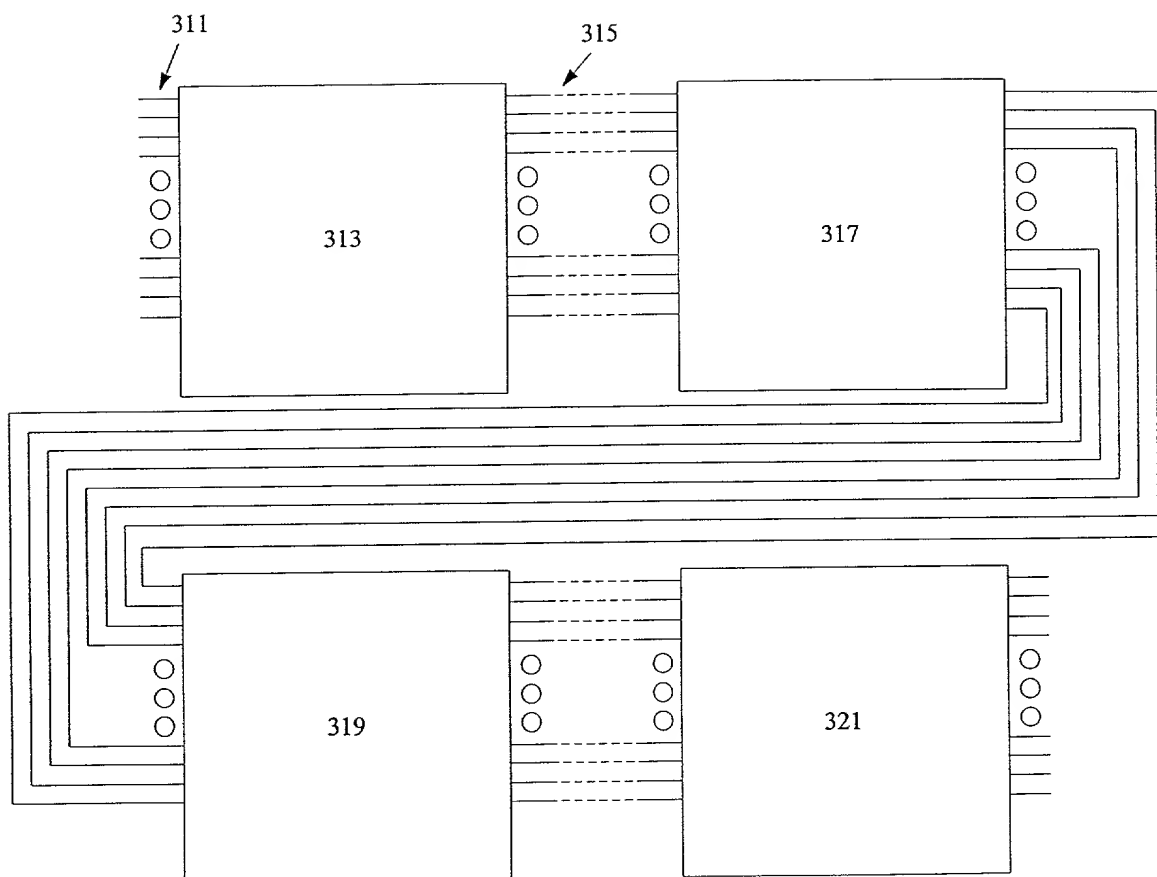


FIG. 3B

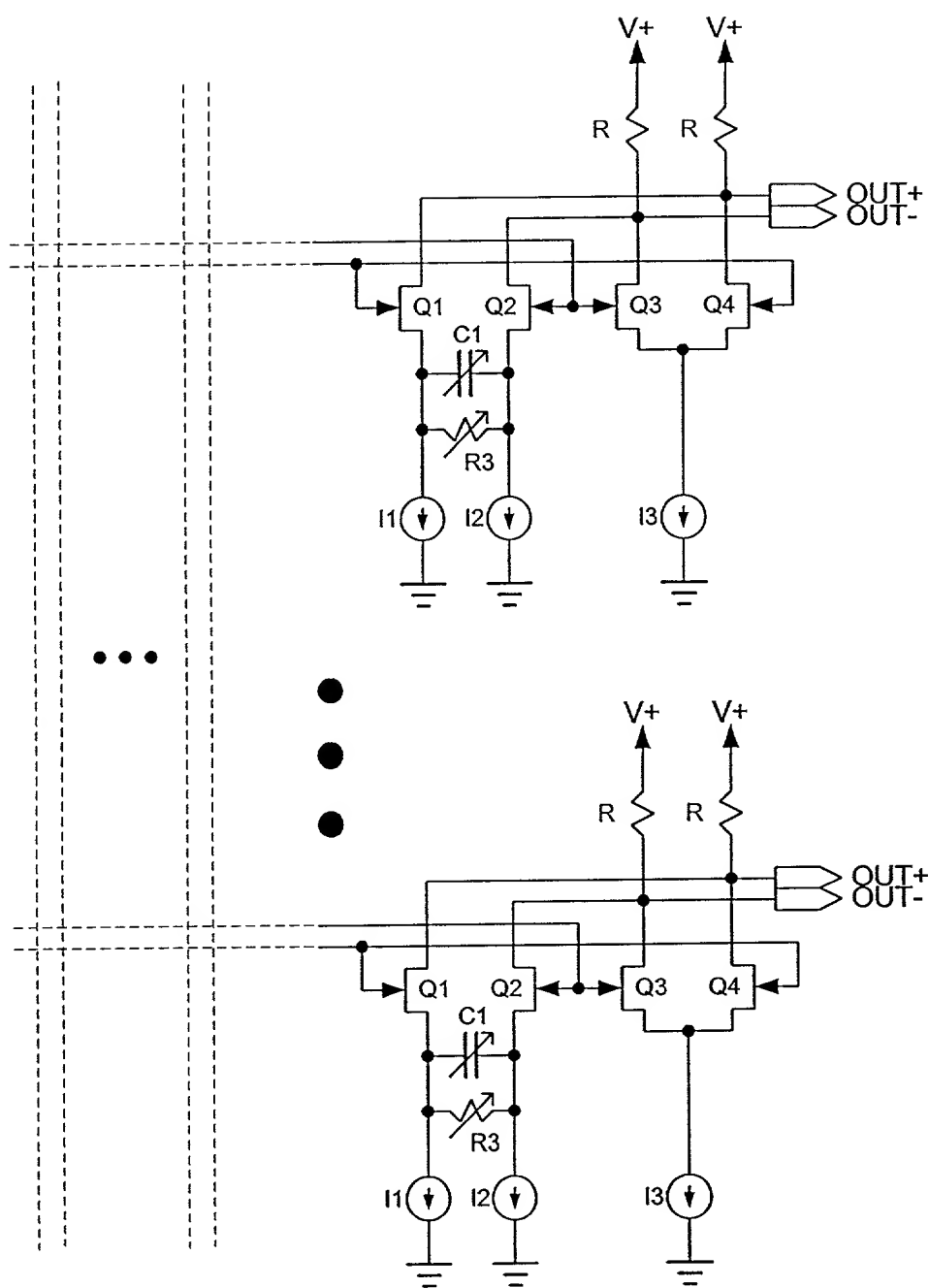


FIG. 4

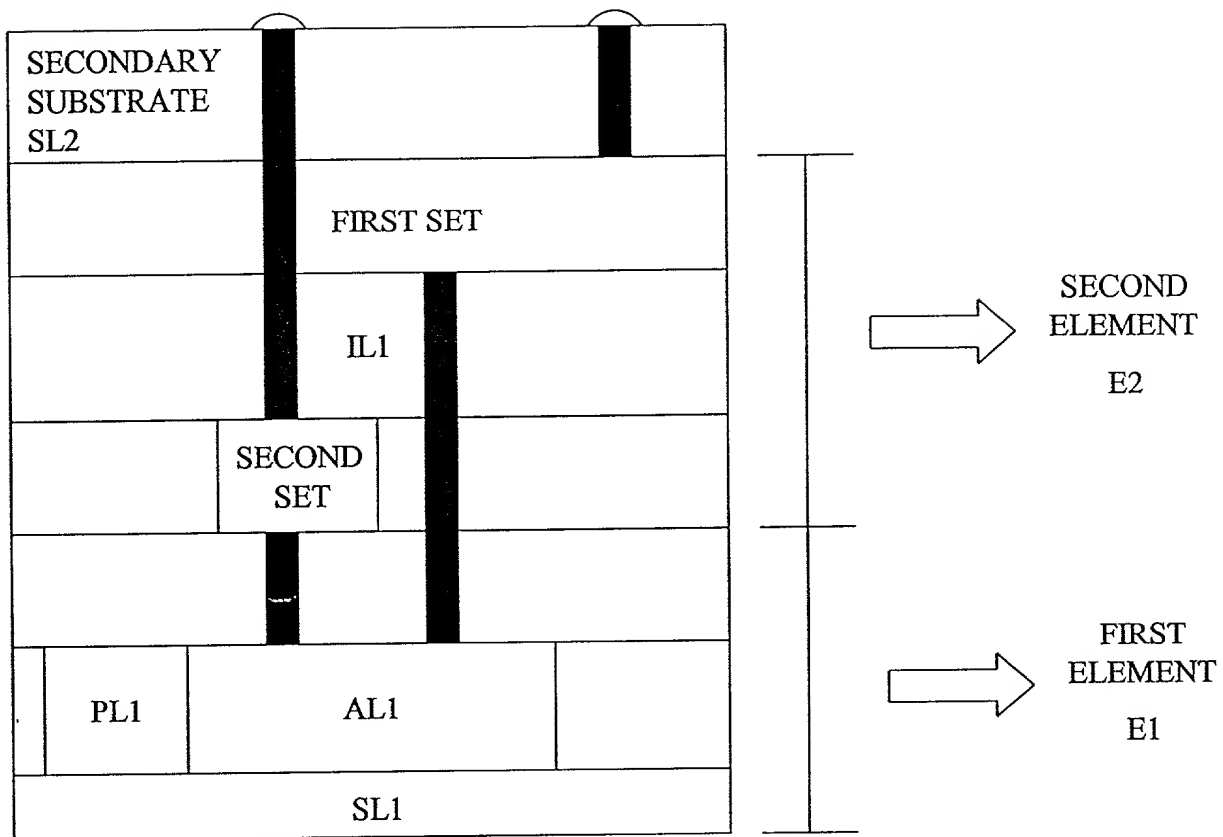


FIG. 5

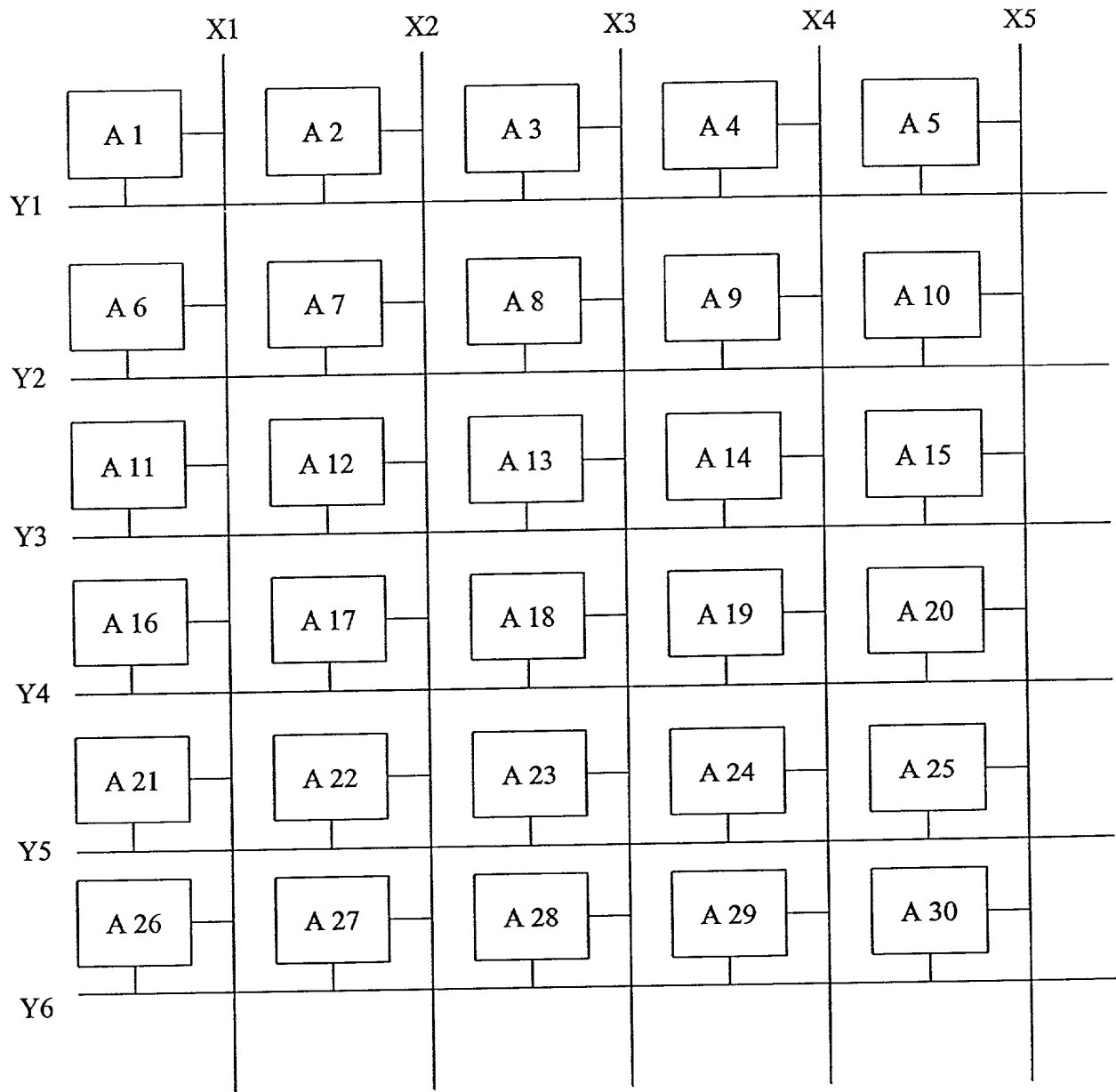


FIG. 6

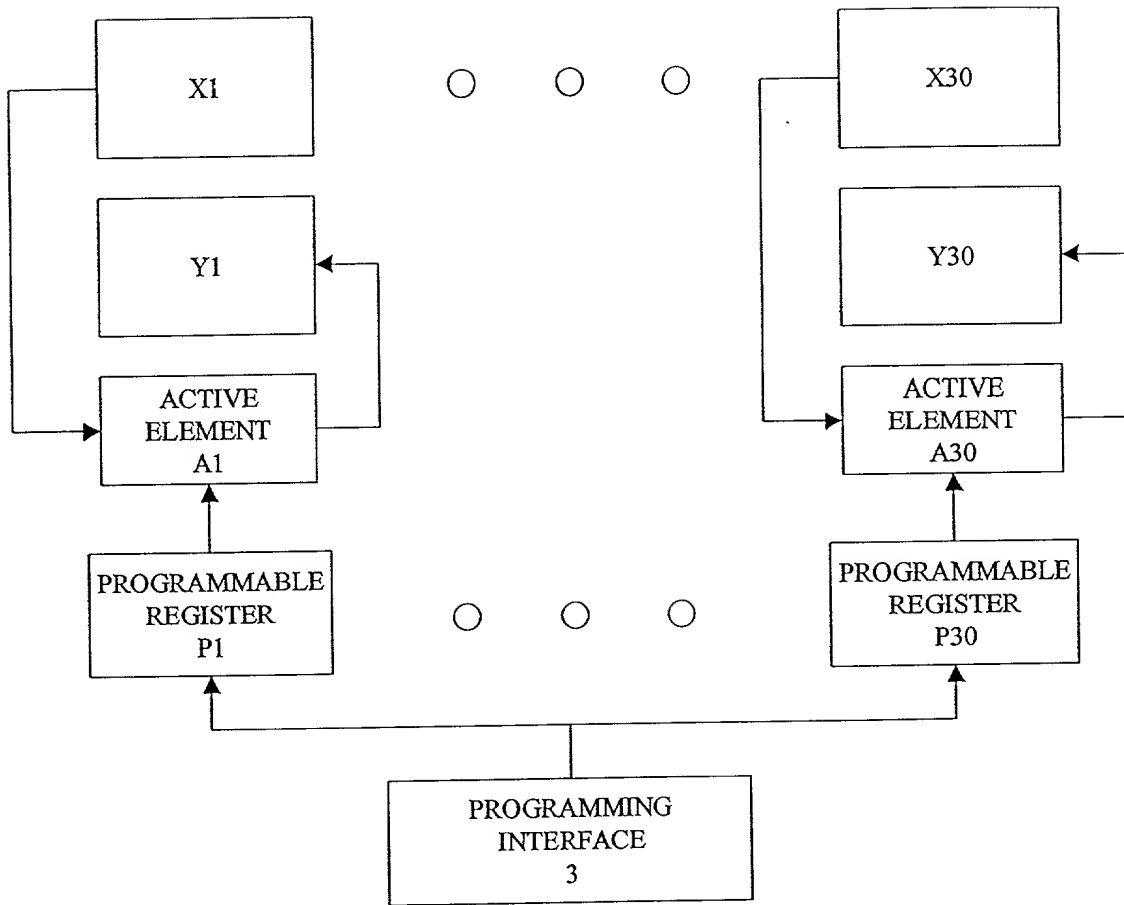


FIG. 7



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graph TD; START([START]) --> 81[RECEIVE PROGRAMMING INSTRUCTION 81]; 81 --> 83[MAP SPECIFIED INPUT TO OUTPUT X 83]; 83 --> 85[AUTO SELECT NEXT OUTPUT (X=X+1) 85]; 85 --> 87{ALL OUTPUTS MAPPED? 87}; 87 -- NO --> 89[IDENTIFY NEXT SPECIFIED INPUT 89]; 89 --> 83; 87 -- YES --> END([END]);
```

FIG. 8

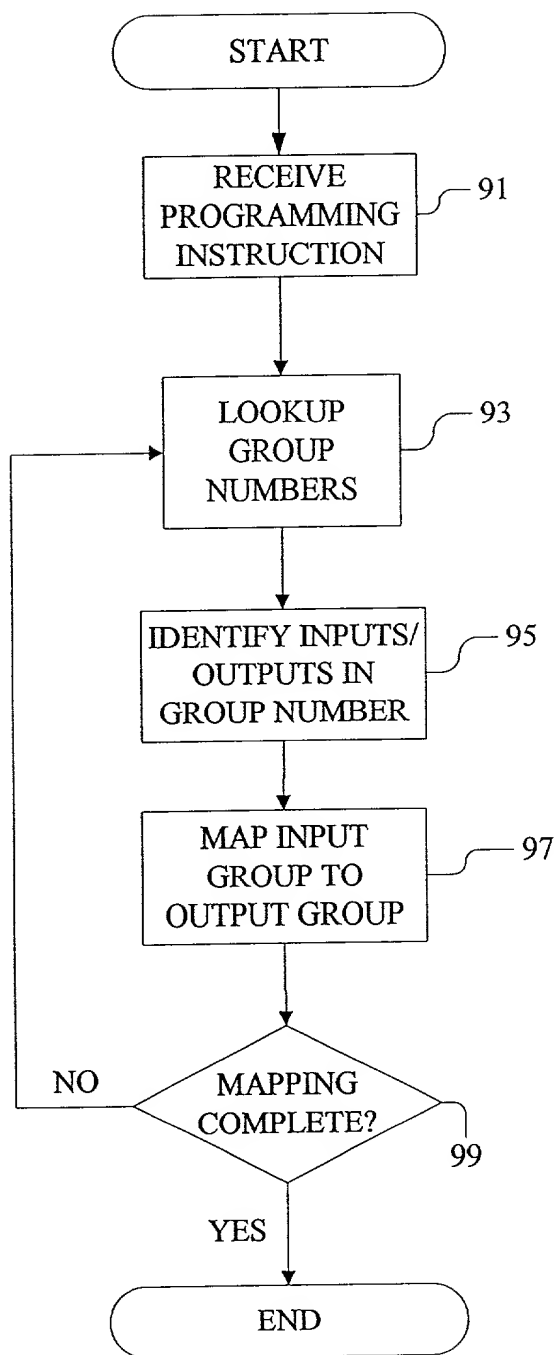


FIG. 9

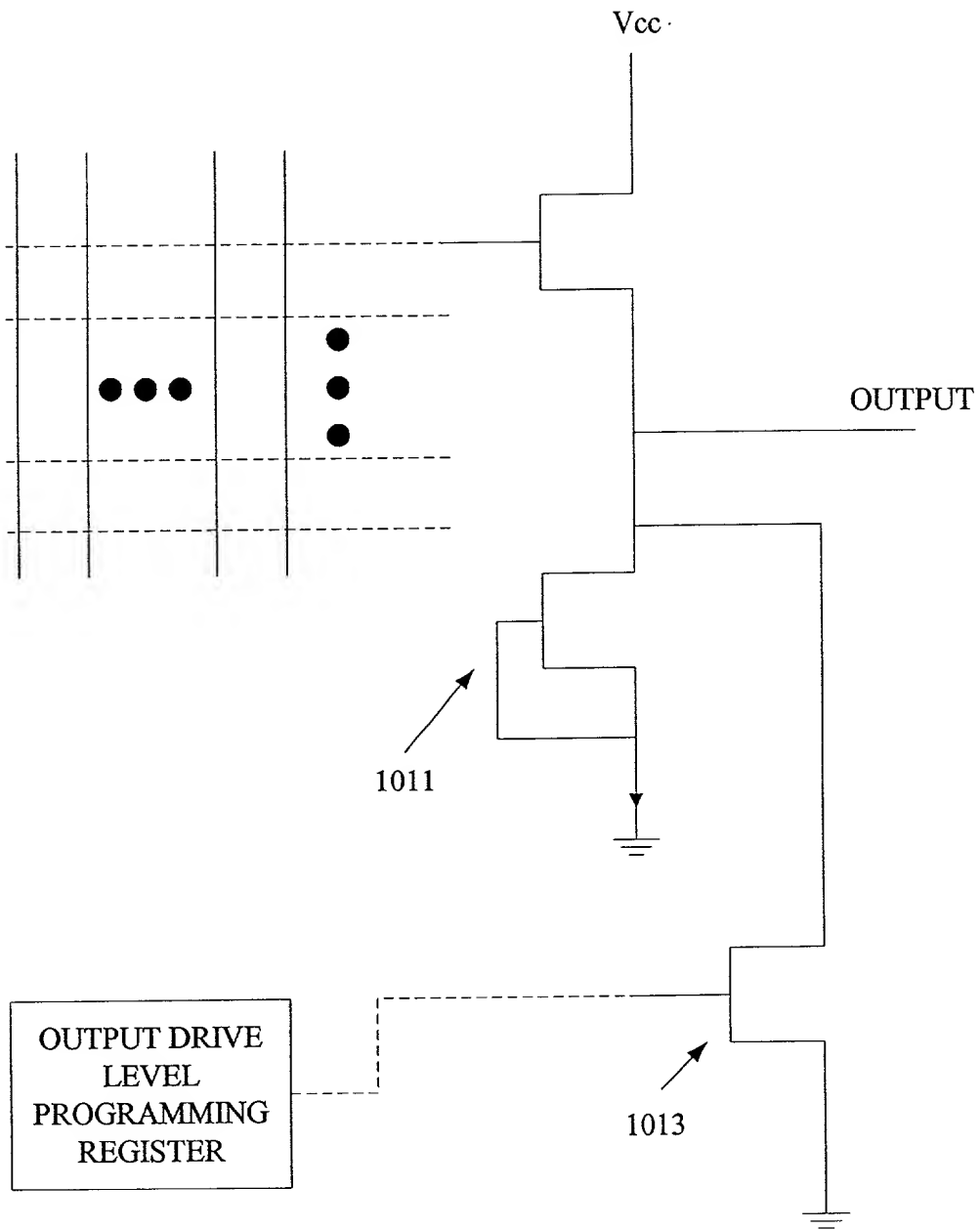


FIG. 10

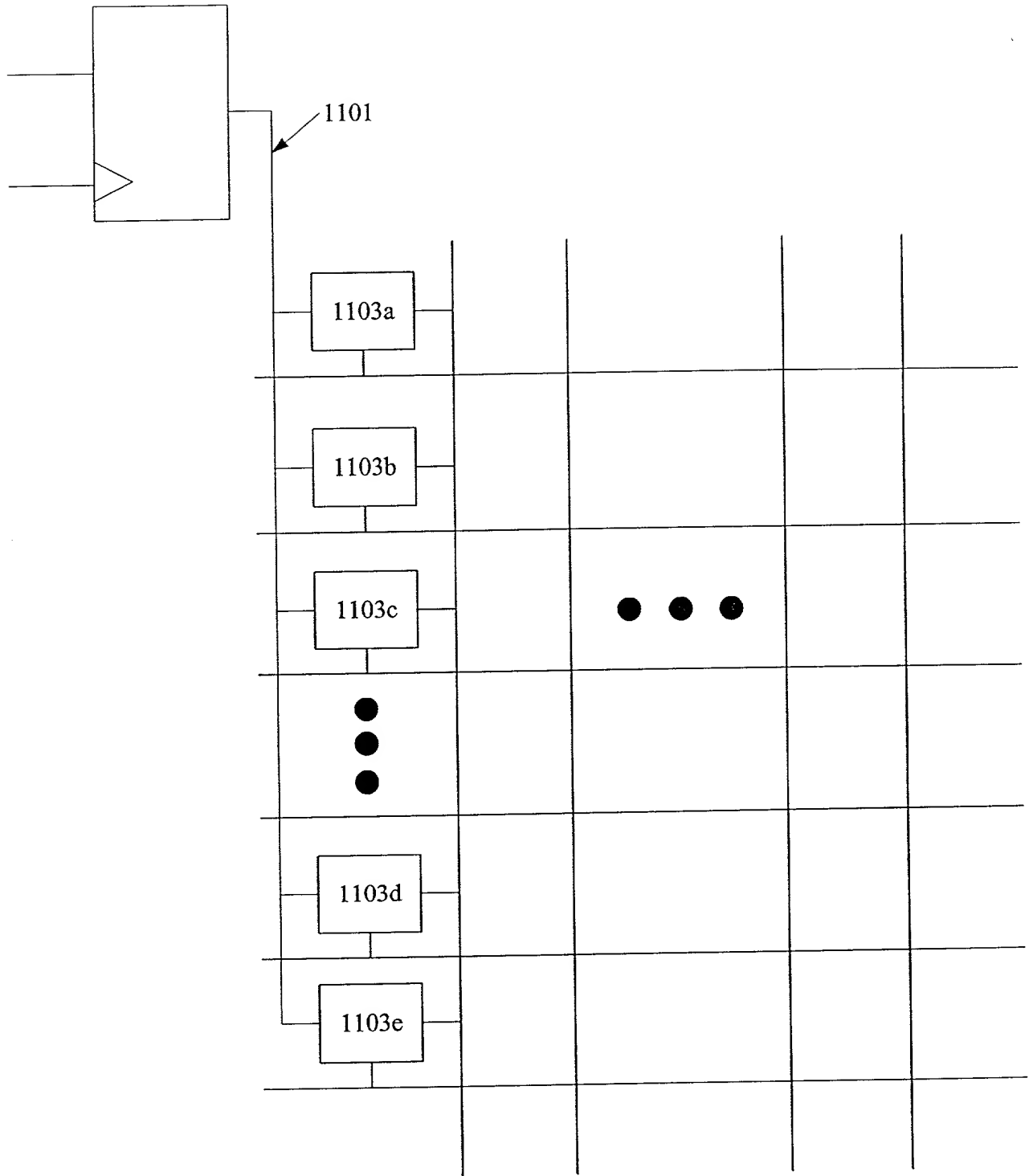


FIG. 11

FIG. 12

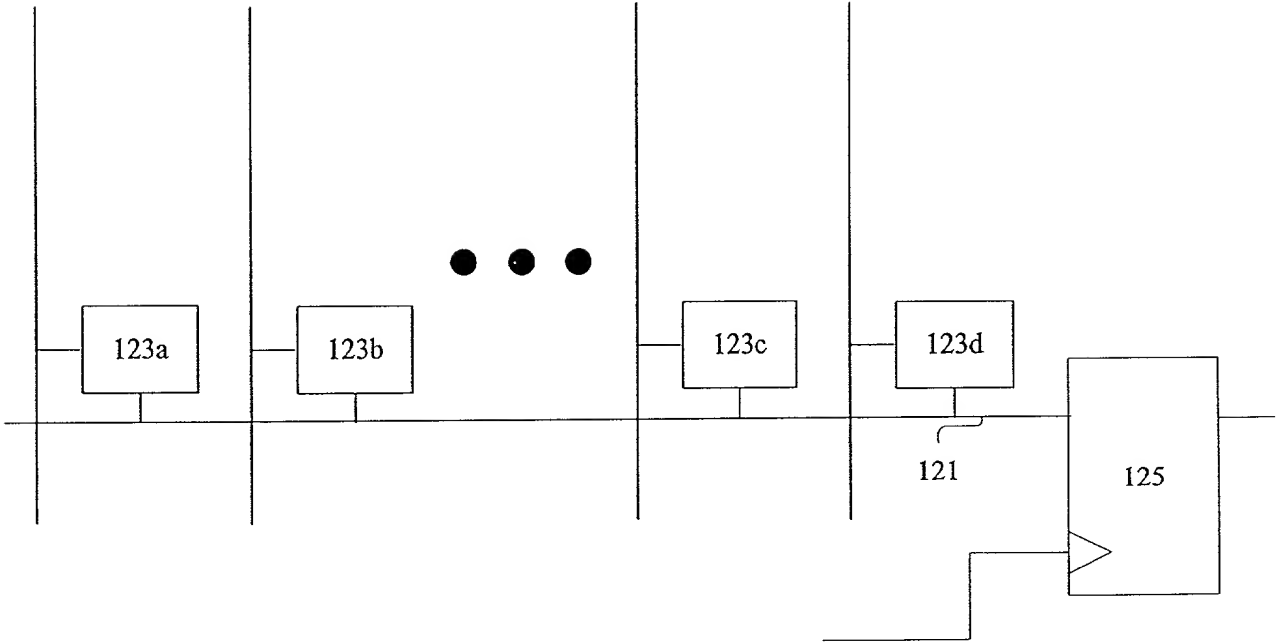


FIG. 13

